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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Wong; Daryanani et al.

Application No./Patent No.: 10/685629 Filed/Issue Date: 10/15/2003

Entitled: Method for Integrating Non-Volatile Memory with High-Voltage and Low-Voltage Logic in a Salicide Process (DM-A533)

Microchip Technology Inc. , a corporation

(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

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2. ☐ an assignee of less than the entire right, title, and interest in
   (The extent (by percentage) of its ownership interest is ___ %); or
3. ☐ The assignee of an undivided interest in the entirety of (a complete assignment from one of the joint inventors was made)

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A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 014610 /Frame 0338 , or for which a copy thereof is attached.

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B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

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☒ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[**NOTE**: A separate copy (i.e., a true copy of the original document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

/Bruce W. Slayden II/	May 1, 2009
Signature	Date
Bruce W. Slayden II	Attorney for the Applicant
Printed or Typed Name	Title

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